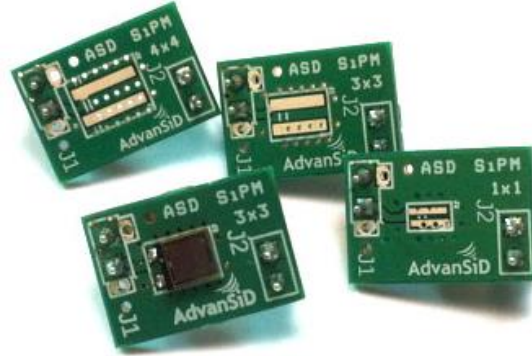


## Sockets for CSP SiPMs



### Features

- Sockets for AdvanSiD RGB and NUV CSP SiPMs
- Convert from SMT to pin output
- Compatible with AdvanSiD SiPM Evaluation Board
- Available for 1x1, 3x3, 4x4 mm<sup>2</sup> CSP SiPMs
- For laboratory use only

### Description

AdvanSiD SiPM Sockets are specifically designed to host AdvanSiD CSP SiPMs and provide pin outputs for SiPM anode and cathode terminals.

The sockets are compatible with AdvanSiD SiPM Evaluation Board. They can be used also as test modules in any laboratory setup.

CSP SiPM can be mounted on Sockets by SMT or manually, with a thin-tip soldering tool.

Upon request, AdvanSiD CSP SiPMs can be delivered already mounted on AdvanSiD SiPM Sockets.

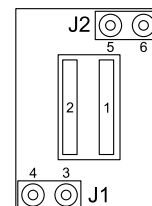
### Ordering Information

ASD-EP-S-1	Socket for 1x1 mm <sup>2</sup> CSP
ASD-EP-S-3	Socket for 3x3 mm <sup>2</sup> CSP
ASD-EP-S-4	Socket for 4x4 mm <sup>2</sup> CSP

### Pins function

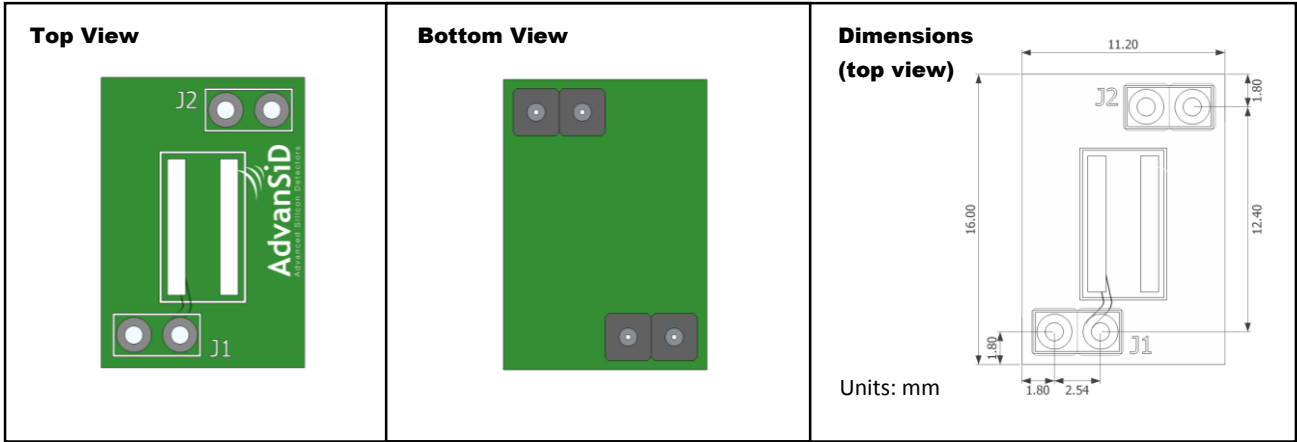
N°	Name	Function
1	Pad 1	SiPM Pin 1*
2	Pad 2	SiPM Pin 2*
3	Pin 3	Pad 2 Out
4	Pin 4	Pad 1 Out
5	Pin 5	n.c.
6	Pin 6	n.c.

\* See AdvanSiD SiPM datasheet



Top View

## Layout and dimensions



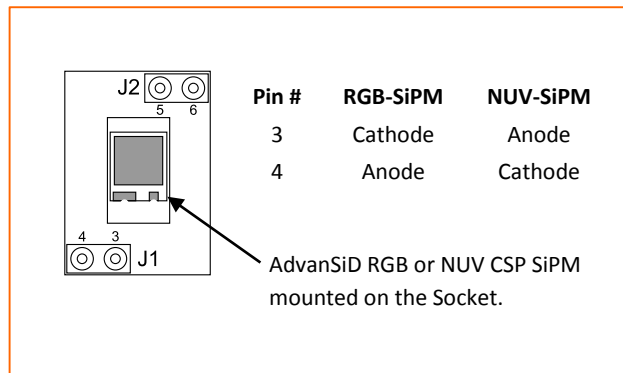
## Mounting CSP SiPMs on Sockets

AdvanSiD CSP SiPMs should be mounted on the Sockets by SMT procedure. However, CSPs can be also manually mounted to the Sockets with a thin tip solder (the pads of the Sockets are long enough to allow for manual soldering) according to the pin-out scheme reported in the Pins Function section of this datasheet. In either cases, it is mandatory to respect the recommended reflow soldering profile reported on the CSP SiPMs datasheet.

## CSP SiPMs already mounted on Sockets

AdvanSiD CSP SiPMs can be supplied already mounted on Sockets. The function of the pins of the Socket changes according to the type of AdvanSiD CSP SiPM, RGB or NUV, that is mounted on the Socket.

Important: here, Anode identifies the p-side of the SiPM p/n junction. Cathode is the n-side of the SiPM p/n junction.



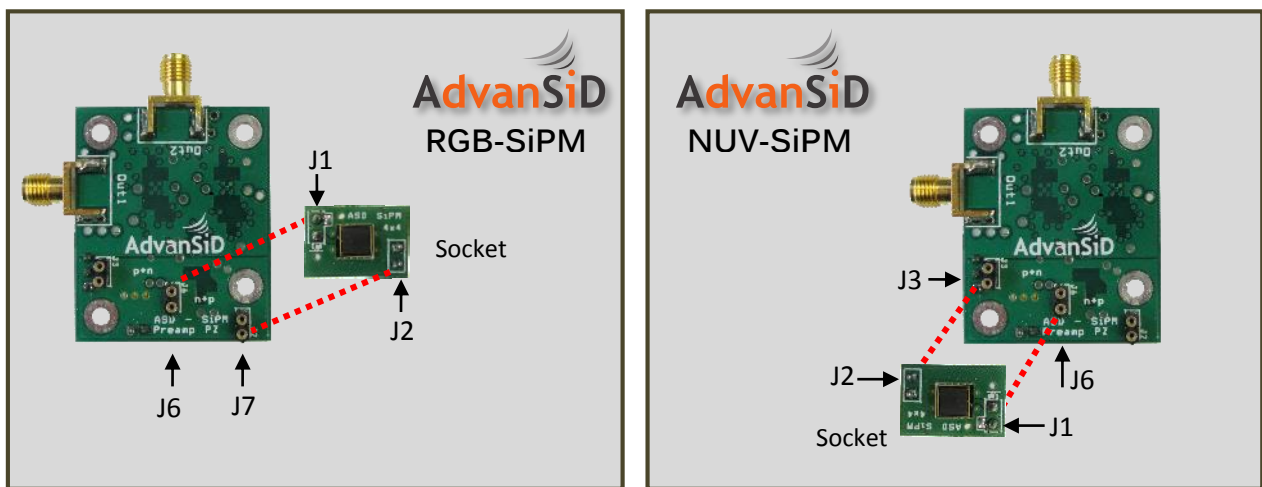
### Connection to the AdvanSiD Evaluation board

The J6 pin header, located on the front side of the AdvanSiD Evaluation Board, receives the anode and cathode terminals of the SiPM to be tested.

The SiPM Sockets insert into the AdvanSiD Evaluation Board according to the SiPM type that is carried:

Sockets with **RGB-SiPM**: J1 (Socket) into J6 (Evaluation Board); J2 (Socket) into J7 (Evaluation Board).

Sockets with **NUV-SiPM**: J1 (Socket) into J6 (Evaluation Board); J2 (Socket) into J3 (Evaluation Board).



Pin header J3 and J7 are not electrically active; they serve as mechanical support for the socket.

For further details, refer to the AdvanSiD Evaluation Board datasheet.